EPIQ Matchstiq Z3u

Space-Based Missions

Open Architecture

Small Form Factor

Spectrum Dominance

High Performance

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Low SWaP SDR Platform Products



Scalable SWaP vs Capability



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Matchstiq Z3u

Fully Housed SDR Module

- Provides RF Coverage from 70MHz 6GHz
 - Up to 50MHz BW on Receive and Transmit
 - 12b ADCs & DACs
 - Typical Rx NF 8dB, Rx IIP3 -10dBm
- Supports Multiple Modes of Operation
 - 1 Rx + 1 Tx, FDD or TDD
 - 2 Rx Phase Coherent
- Features & Interfaces
 - On Board GPSDO, Origin Spider ORG4033f
 - On Board Filtering & Amplification
 - AMD Zynq US+ ZU3 FPGA for Control & Processing
 - Ext 10Mz Input + PPS Input via MMCX Connectors
 - USB 3.0 Interface via USB-C
 - 128 GB eMMC Storage + microSD Card Slot
- Typical Power ~4W via USB-C or DC Input; 92mm x 70mm x 19mm



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Z3u Block Diagram

micro Status Push USB LED Button 7V to 17V DC Debug USB2 DC Power Header Input USB-to-UART Power 5V @ 900 mA JTAG GPIOs Supplies Serial Console Rx Pre-Select Filter 🗲 USB3 USB OTG USB3 USB-C Phy Rx1 Rx1 antenna Temp Zyng UltraScale+ (SMA) Sensors AD9361 MPSOC **Rx Pre-Select** RFIC 6-axis (CPU + FPGA)Filter 🗲 IMU (2x2) XCZU3EG-1SBVA484I EEPROM Rx2 (tot T/Rx2 antenna Tx2 (SMA) microSD SDIO **GPS Fix** LED UART GPS SDIO GPS antenna eMMC PLL Receiver (SMA) SPI (128 GB) 128 MB 2 GB QSPI TCVCXO PPS LPDDR4 DAC 10 MHz Input FLASH 0 RAM (MMCX) PPS Input PPS Input 0 (MMCX) Externally Accessible Analog/RF Hardware KEY **Digital Hardware** Interface







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Z3u Deployment

Mode 1 – Stand Alone

- Completely Stand Alone SDR
- Local Processing ٠
 - Runs Ubuntu Linux on the Zyng US+ ZU3 ٠
 - Runs libsidekiq Locally •
 - Option for OpenCPI Build •
 - User Application Runs on the Zynq US+ ZU3 ٠
 - **Epiq Solutions Skylight or ERA** •
 - 3rd Party Applications •
 - Power Provided by DC Jack, 7-17V •
 - USB-C Connector Available to Connect to a Host •

Optional Host





Z3u Deployment

Phone or Tablet as Host

- Mobile, Small Form Factor SDR
- Z3u Mag-Mounts to Back or Phone or Tablet
 - Option to Screw Mount to a Tactical Radio
- Single USB-C Cable from Phone Provides Power & Data
- Z3u or Phone can Provide Application
 - Z3u Accessed as an IP Address from Phone
 - Application in an App or a Browser Window
 - Epiq Solutions Skylight or ERA Application
 - GNU Radio, SOAPY Interface, or Other Application
 - Embedded Application on Z3u
- Phone Capability for 4G/5G/WiFi Unaffected by Z3u
 - Simultaneous 5G Operation + SDR Application



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Z3u Power Consumption for Example Configurations

| Mode | Details | Sample Rate | Power Consumption |
|------------|---|-------------|-------------------|
| Idle | 2 / 1 | 2/2 | 1/1 |
| | USB, GPS on, and antenna bias enabled | NA | 2.36 W |
| | USB, GPS on, and antenna bias disabled | NA | 2.31 W |
| | USB, GPS off, and antenna bias enabled | NA | 2.21 W |
| | USB, GPS off, and antenna bias disabled | NA | 2.15 W |
| 1 Rx, O Tx | USB, GPSDO enabled, GPS on, and antenna bias enabled | NA | 3.17 W |
| | USB, GPS on, and antenna bias enabled | 1 MSPS | 3.61 W |
| | USB, GPS on, and antenna bias enabled | 10 MSPS | 3.76 W |
| 2 Rx, O Tx | USB, GPS on, and antenna bias enabled | 30 MSPS | 4.06 W |
| | USB, GPS on, and antenna bias enabled | 1 MSPS | 3.81 W |
| | USB, GPS on, and antenna bias enabled | 10 MSPS | 4.11 W |
| O Rx, 1 Tx | USB, GPS on, and antenna bias enabled | 30 MSPS | 4.46 W |
| | USB, GPS on, and antenna bias enabled | 1 MSPS | 3.26 W |
| | USB, GPS on, and antenna bias enabled | 10 MSPS | 3.56 W |
| 1 Rx, 1 Tx | USB, GPS on, and antenna bias enabled | 30 MSPS | 3.91 W |
| | USB, GPS on, and antenna bias enabled | 1 MSPS | 3.61 W |
| | USB, GPS on, and antenna bias enabled | 10 MSPS | 3.86 W |

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Z3u Integrated RFIC – Analog Devices AD9361

Dual Rx, Dual Tx Transceiver

- RF Coverage
 - LO tuning from 70MHz 6GHz
 - RF Input Frequencies from 45MHz 6GHz
- Receiver Specifications
 - 12b ADCs, Typical NF 8dB, Typical IIP3 –10dBm
 - 50MHz Maximum Bandwidth per Receiver
- Transmitter Specifications
 - 12b DACs, Output Power +7dBm, Typical OIP3 +23dBm
 - 50MHz Maximum Bandwidth per Transmitter
- Advanced Features
 - Frequency Hopping
 - Multi-Chip Sync
- One AD9361 Devices is Integrated into the Z3u





Thank You

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